

ABSTRACT

A method and system for evaluating the speed of a circuit are provided. .In accordance with one embodiment, the method comprises determining during a first operational phase of a first operational cycle the propagation speed of a first signal in a first signal propagation path, and concurrently preventing all signals from propagating in a second signal propagation path substantially parallel with the first signal propagation path. The method further comprises determining during a second operational phase alternating with the first operational phase the propagation speed of a second signal in the second signal propagation path, and concurrently preventing all signals from propagating in the first signal propagation path.